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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/805,247	03/22/2004	James Mason Lipsit	12177/46404	9058

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WASHINGTON, DC 20005

EXAMINER
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CUMMING, WILLIAM D

ART UNIT	PAPER NUMBER
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2617

DATE MAILED: 09/27/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

## Office Action Summary

Application No.

10/805,247

Applicant(s)

JAMES LIPSIT

Examiner

WILLIAM D. CUMMING

Art Unit

2617

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

### Status

- 1) ☐ Responsive to communication(s) filed on \_\_\_\_.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

### Disposition of Claims

- 4) ☒ Claim(s) 1-19 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-19 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_ are subject to restriction and/or election requirement.

### Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

### Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

### Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO/SB/08)  
Paper No(s)/Mail Date \_\_\_\_.
- 4) ☐ Interview Summary (PTO-413)  
Paper No(s)/Mail Date. \_\_\_\_.
- 5) ☐ Notice of Informal Patent Application
- 6) ☐ Other: \_\_\_\_.

## **DETAILED ACTION**

### ***Claim Rejections - 35 USC § 112***

1. The following is a quotation of the first paragraph of 35 U.S.C. 112:

The specification shall contain a written description of the invention, and of the manner and process of making and using it, in such full, clear, concise, and exact terms as to enable any person skilled in the art to which it pertains, or with which it is most nearly connected, to make and use the same and shall set forth the best mode contemplated by the inventor of carrying out his invention.

2. Claims 1-19 are rejected under 35 U.S.C. 112, first paragraph, as failing to comply with the written description requirement. The claim(s) contains subject matter which was not described in the specification in such a way as to reasonably convey to one skilled in the relevant art that the inventor(s), at the time the application was filed, had possession of the claimed invention.

The specification fails to originally support the newly submitted claims.

Applicant's attorney has failed to specifically point out support in the specification claimed in the new claims.

3. Claims 11-19 are rejected under 35 U.S.C. 112, first paragraph, as failing to comply with the enablement requirement. The claim(s) contains subject matter which was not described in the specification in such a way as to enable one skilled in the art to which it pertains, or with which it is most nearly connected, to make and/or use the invention.

The specification fails to provide an enabling disclosure computer executable instructions. The computer executable instructions are not disclosed and is known only to the inventors. In computer applications, it is not unusual for

the claimed invention to involve two areas of prior art or more than one technology, e.g., an appropriately programmed computer and an area of application of said computer. *White Consol. Indus.*, 214 USPQ at 821. In regard to the "*skilled in the art*" standard, in cases involving both the art of computer programming, and another technology, the examiner must recognize that the knowledge of persons skilled in both technologies is the appropriate criteria for determining sufficiency. See *In re Naquin*, 398 F.2d 863, 158 USPQ 317 (CCPA 1968); *In re Brown*, 477 F.2d 946, 177 USPQ 691 (CCPA 1973); and *White Consol. Indus. v. Vega Servo-Control, Inc.*, 214 USPQ 796, 822 (S.D.Mich. 1982), *aff'd on related grounds*, 713 F.2d 788, 218 USPQ 961 (Fed. Cir. 1983). In a typical computer application, system components are often represented in a "*block diagram*" format, i.e., a group of hollow rectangles representing the elements of the system, functionally labeled, and interconnected by lines. Such block diagram computer cases may be categorized into (A) systems which include but are more comprehensive than a computer and (B) systems wherein the block elements are totally within the confines of a computer.

The first category of such block diagram cases involves systems which include a computer as well as other system hardware and/or software components. In order to meet his or her burden of establishing a reasonable basis for questioning the adequacy of such disclosure, the examiner should initiate a factual analysis of the system by focusing on each of the individual block element components. More specifically, such an inquiry should focus on

the diverse functions attributed to each block element as well as the teachings in the specification as to how such a component could be implemented. Based on such an analysis, the examiner has reasonably contend that more than routine experimentation would be required by one of ordinary skill in the art to implement such a component or components, that component or components is specifically be challenged by the examiner as part of this 35 U.S.C. 112, first paragraph rejection. Additionally, the examiner has determine whether certain of the hardware or software components depicted as block elements are themselves complex assemblages which have widely differing characteristics and which must be precisely coordinated with other complex assemblages. Under such circumstances, a reasonable basis does exist for challenging such a functional block diagram form of disclosure. See *In re Ghiron*, 442 F.2d 985, 169 USPQ 723 (CCPA 1971) and *In re Brown, supra*. Moreover, even if the applicants has cited prior art patents or publications to demonstrate that particular block diagram hardware or software components are old, it should not always be considered as self-evident how such components are to be interconnected to function in a disclosed complex manner. See *In re Scarbrough*, 500 F.2d 560, 566, 182 USPQ 298, 301 (CCPA 1974) and *In re Forman*, 463 F.2d 1125, 1129, 175 USPQ 12, 16 (CCPA 1972). Furthermore, in complex systems including a digital computer, a microprocessor, or a complex control unit as one of many block diagram elements, timing between various system elements may be of the essence and without a timing chart relating the timed sequences for each element, an

unreasonable amount of work may be required to come up with the detailed relationships an applicant alleges that he or she has solved. See *In re Scarbrough*, 500 F.2d at 566, 182 USPQ at 302. For example, in a block diagram disclosure of a complex claimed system which includes a microprocessor and other system components controlled by the microprocessor, a mere reference to a prior art, commercially available microprocessor, without any description of the precise operations to be performed by the microprocessor, fails to disclose how such a microprocessor would be properly programmed to either perform any required calculations or to coordinate the other system components in the proper timed sequence to perform the functions disclosed and claimed. If, in such a system, a particular program is disclosed, such a program should be carefully reviewed to ensure that its scope is commensurate with the scope of the functions attributed to such a program in the claims. See *In re Brown*, 477 F.2d at 951, 177 USPQ at 695. Since, the disclosure fails to disclose any program and more than routine experimentation would be required of one skilled in the art to generate such a program, the examiner clearly has a reasonable basis for challenging the sufficiency of such a disclosure.

4. The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

5. Claims 1-10 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

A single claim which claims both an apparatus and process of using the apparatus is indefinite under 35 U.S.C. 112, second paragraph. In *Ex parte Lyell*, 17 USPQ2d 1548 (Bd. Pat. App. & Inter. 1990), a claim directed to an automatic transmission workstand and the process of using it was held to be ambiguous and properly rejected under 35 U.S.C. 112, second paragraph.

***Claim Rejections - 35 USC § 101***

6. 35 U.S.C. 101 reads as follows:

Whoever invents or discovers any new and useful process, machine, manufacture, or composition of matter, or any new and useful improvement thereof, may obtain a patent therefor, subject to the conditions and requirements of this title.

7. Claims 1-10 are rejected under 35 U.S.C. 101 because the claimed invention is directed to non-statutory subject matter. The claim is directed to neither a "process" nor a "machine," but rather embraces or overlaps two different statutory classes of invention set forth in 35 U.S.C. 101 which is drafted so as to set forth the statutory classes of invention in the alternative only. *Id.* at 1551.

***Claim Rejections - 35 USC § 102***

8. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

9. Claims 1-19 are rejected under 35 U.S.C. 102(e) as being clearly anticipated by **McGregor, et al** (United States Patent 5,625,669).

**McGregor, et al** disclose an apparatus comprising a processor (figure 1, #14); a memory (#14) coupled to the processor to store computer program instructions (figures 3, 5, 13) executable by the processor (#14) and a communication interface (figure 6, #28) to locally connect to a cellular telephone (#208). The computer program instructions (figures 3, 5, 13) to generate a graphical user interface (figure 18A-R) to program the cellular telephone (#208) via the communication interface (#28), the graphical user interface (figure 18A-R) comprising a field to receive a user input value, the value being modifiable based on a user verification process.



***Double Patenting***

10. The nonstatutory double patenting rejection is based on a judicially created doctrine grounded in public policy (a policy reflected in the statute) so as to prevent the unjustified or improper timewise extension of the "right to exclude" granted by a patent and to prevent possible harassment by multiple assignees. A nonstatutory obviousness-type double patenting rejection is appropriate where the conflicting claims are not identical, but at least one examined application claim is not patentably distinct from the reference claim(s) because the examined application claim is either anticipated by, or would have been obvious over, the reference claim(s). See, e.g., *In re Berg*, 140 F.3d 1428, 46 USPQ2d 1226 (Fed. Cir. 1998); *In re Goodman*, 11 F.3d 1046, 29 USPQ2d 2010 (Fed. Cir. 1993); *In re Longi*, 759 F.2d 887, 225 USPQ 645 (Fed. Cir. 1985); *In re Van Ornum*, 686 F.2d 937, 214 USPQ 761 (CCPA 1982); *In re Vogel*, 422 F.2d 438, 164 USPQ 619 (CCPA 1970); and *In re Thorington*, 418 F.2d 528, 163 USPQ 644 (CCPA 1969).

11. A timely filed terminal disclaimer in compliance with 37 CFR 1.321(c) or 1.321(d) may be used to overcome an actual or provisional rejection based on a nonstatutory double patenting ground provided the conflicting application or patent either is shown to be commonly owned with this application, or claims an invention made as a result of activities undertaken within the scope of a joint research agreement.

12. Effective January 1, 1994, a registered attorney or agent of record may sign a terminal disclaimer. A terminal disclaimer signed by the assignee must fully comply with 37 CFR 3.73(b).

13. Claims 1-19 are rejected on the ground of nonstatutory obviousness-type double patenting as being unpatentable over the claims of U.S. Patent No. 5,974,311 and 6,748,209. Although the conflicting claims are not identical, they are not patentably distinct from each other because the claims in the continuing application are broader than the ones in the patent.

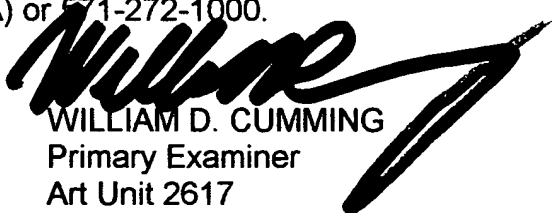
***Conclusion***

14. If applicants wish to request for an interview, an *"Applicant Initiated Interview Request"* form (PTOL-413A) should be submitted to the examiner prior to the interview in order to permit the examiner to prepare in advance for the interview and to focus on the issues to be discussed. This form should identify the participants of the interview, the proposed date of the interview, whether the interview will be personal, telephonic, or video conference, and should include a brief description of the issues to be discussed. A copy of the completed *"Applicant Initiated Interview Request"* form should be attached to the Interview Summary form, PTOL-413 at the completion of the interview and a copy should be given to applicant or applicant's representative.

15. Any inquiry concerning this communication or earlier communications from the examiner should be directed to **WILLIAM D. CUMMING** whose telephone number is 571-272-7861. The examiner can normally be reached on Monday-Thursday 11am-8:30pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nick Corsaro can be reached on 571-272-7876. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

16. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.



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